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U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10079114	02/20/2002	257		2811	M. V. WSOV

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**CONTINUING DATA VERIFIED:

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** FOREIGN APPLICATIONS VERIFIED:
GERMANY 101 38 147.2 02/20/2001

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	ATTORNEY DOCKET NO
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no			MAS-FIN-116
35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no			
Verified and Acknowledged Examiners's initials			
TITLE : Electronic component with a semiconductor chip and method of producing an electronic component			

U.S. DEPT. OF COMM. / PAT. & TM. ETO-136 (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
Assistant Examiner		DRAWING	
		Sheets Drwg.	Figs. Drwg.
Primary Examiner		Print Fig.	
PREPARED FOR ISSUE		Application Examiner	
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